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| APPLICATION NUMBER | FILING or 371(c)<br>DATE | GRP ART UNIT | FIL FEE REC'D | ATTY.DOCKET.NO | DRAWINGS | TOT CLAIMS | IND CLAIMS |
|--------------------|--------------------------|--------------|---------------|----------------|----------|------------|------------|
| 10/549,933         | 09/11/2006               | 1734         | 1030          | 2342-0143PUS1  | 8        | 15         | 2          |

## CONFIRMATION NO. 7899

02292  
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## FILING RECEIPT

Date Mailed: 11/09/2006

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### Applicant(s)

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### Assignment For Published Patent Application

HITACHI KOKUSAI ELECTRIC INC., Tokyo, JAPAN

**Power of Attorney:** The patent practitioners associated with Customer Number 02292

### Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP04/11266 08/05/2004

### Foreign Applications

JAPAN 2003-206526 08/07/2003

### If Required, Foreign Filing License Granted: 11/07/2006

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/549,933**

**Projected Publication Date:** 02/15/2007

**Non-Publication Request:** No

**Early Publication Request:** No

### Title

Substrated processing apparatus and semiconductor device producing method

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